XN0111F (XN111F)

Silicon PNP epitaxial planer transistor

For switching/digital circuits

Features

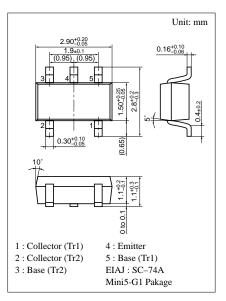
- Two elements incorporated into one package. (Emitter-coupled transistors with built-in resistor)
- Reduction of the mounting area and assembly cost by one half.

Basic Part Number of Element

• UNR111F(UN111F) \times 2 elements

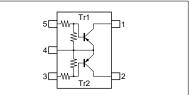
Parameter		Symbol	Ratings	Unit
Rating of element	Collector to base voltage	V _{CBO}	-50	V
	Collector to emitter voltage	V _{CEO}	-50	V
	Collector current	I _C	-100	mA
Overall	Total power dissipation	P _T	300	mW
	Junction temperature	Tj	150	°C
	Storage temperature	T _{stg}	-55 to +150	°C

Absolute Maximum Ratings (Ta=25°C)



Marking Symbol: 70

Internal Connection

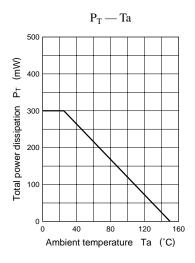


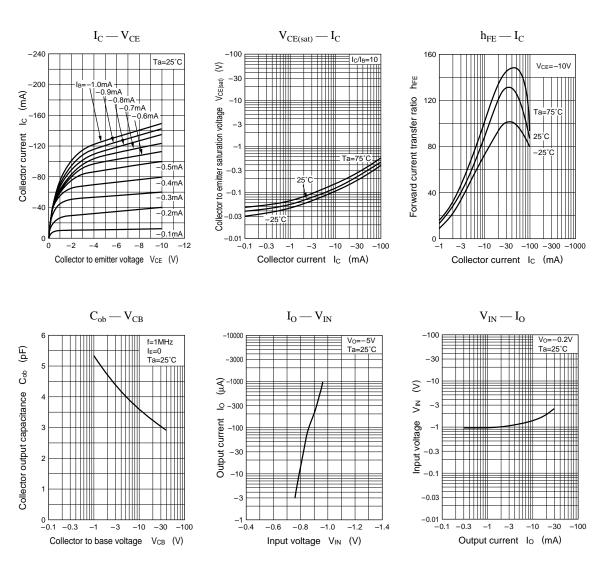
Unit Parameter Symbol Conditions max min typ Collector to base voltage $I_C = -10 \mu A$, $I_E = 0$ -50V V_{CBO} Collector to emitter voltage V_{CEO} $I_{C} = -2mA, I_{B} = 0$ -50V $V_{CB} = -50V, I_E = 0$ -0.1 I_{CBO} μΑ Collector cutoff current $V_{CE} = -50V, I_B = 0$ -0.5 I_{CEO} μΑ Emitter cutoff current I_{EBO} $V_{EB} = -6V, I_C = 0$ - 1.0 mA $V_{CE} = -10V, I_C = -5mA$ Forward current transfer ratio h_{FE} 30 $V_{CE} = -10V, I_C = -5mA$ Forward current transfer h_{FE} ratio hFE (small/large)*1 0.5 0.99 $I_{C} = -10mA$, $I_{B} = -0.3mA$ -0.25v Collector to emitter saturation voltage V_{CE(sat)} Output voltage high level $V_{CC} = -5V, V_B = -0.5V, R_L = 1k\Omega$ -4.9 v VOH Output voltage low level VOL $V_{CC} = -5V, V_B = -2.5V, R_L = 1k\Omega$ -0.2V $V_{CB} = -10V$, $I_E = 1mA$, f = 200MHzTransition frequency \mathbf{f}_{T} 80 MHz -30% 4.7 +30%kΩ Input resistance R_1 Resistance ratio R_{1}/R_{2} 0.47

Electrical Characteristics (Ta=25°C)

*1 Ratio between 2 elements

Note) The Part number in the Parenthesis shows conventional part number.





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